

Name of the project: Electronics for the Beyond Silicon Era (ELBYSIER)

PROJECT TIMETABLE

MONTHS	M1	M2	M3	M4	M5	M6	M7	M8	M9	M10	M11	M12	M13	M14	M15	M16	M17	M18	M19	M20	M21	M22	M23	M24	M25	M26	M27	M28	M29	M30	M31	M32	M33	M34	M35	M36			
	09/15	10/15	11/15	12/15	01/16	02/16	03/16	04/16	05/16	06/15	07/16	08/16	09/16	10/16	11/16	12/16	01/17	02/17	03/17	04/17	05/17	06/17	07/17	08/17	09/17	10/17	11/17	12/17	01/18	02/18	03/18	04/18	05/18	06/18	07/18	08/18			
Project activity*																																							
A1													1st Year EVALUATION													2nd Year EVALUATION													Final External EVALUATION
O1																																							
O				O2							O3							O4							O5							O6							O7
M	M1 CRETE						M2 BUCHAREST						M3 CRETE						M4 GARDANNE						M5 LISBON						M6 WARSAW								
E																																							
C							C1-C2 spring IP BUCHAREST						C3-C4 autumn IP CRETE						C5-C6 spring IP GARDANNE						C7-C8 autumn IP LISBON						C9-C10 spring IP WARSAW			C11-C12 Summer IP CRETE					